

EAST - [09656172.wsp:1]

File View Edit Tools Window Help

BRS:
BRS:
Pending
Active

- L1: (2108) upconverter
- L2: (900) L1 and mixer
- L3: (1) 2 and ("DC" near5 "power control")
- L4: (1) 2 and (("DC" or "direct current") near8 (power adj control\$4))
- L5: (86) 2 and (("DC" or "direct current") near8 control\$4)
- L6: (0) 5 and "differential pair output signal"
- L7: (6) 5 and ("differential pair" near8 signal)
- L8: (32) 2 and (("DC" or "direct current") near8 control\$4)
- L9: (4) 2 and (("DC" or "direct current") near8 control\$4).clm.
- L10: (1) 9 and ("differential pair" near8 signal).clm.
- L11: (0) 5 and "differential pair output signal".clm.
- L12: (0) 10 and "differential pair output signal".clm.
- L13: (0) "DC power control" same "differential pair output signal"
- L14: (125252) "455"/\$.ccls. or "375"/\$.ccls.
- L15: (1110) 14 and upconverter
- L16: (0) 15 and ("DC power control" same "differential pair output signal")**

Failed
Saved

- S1: (1399) upconverter
- S2: (617) S1 and mixer
- S3: (524) S2 and amplifier

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DBs US-PPGPUB-USPAT Plurals
Default operator: OR Highlight all hit terms initially

15 and ("DC power control" same "differential pair output signal")

BRS form IS&R form Image Text HTML

U	1	Document ID	Issue Date	Pages	Title	Current OR	Current Ret	Inventor
[REDACTED]								

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Drafts

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- ✓ L1: (2108) upconverter
- ✓ L2: (900) L1 and mixer
- ✓ L3: (1) 2 and ("DC" near5 "power control")
- ✓ L4: (1) 2 and (("DC" or "direct current") near8 (power adj control\$4))
- ✓ L5: (86) 2 and (("DC" or "direct current") near8 control\$4)
- ✓ L6: (0) 5 and "differential pair output signal"
- ✓ L7: (6) 5 and ("differential pair" near8 signal)
- ✓ L8: (32) 2 and (("DC" or "direct current") near8 control\$4)
- ✓ L9: (4) 2 and (("DC" or "direct current") near8 control\$4).clm.
- ✓ L10: (1) 9 and ("differential pair" near8 signal).clm.
- ✓ L11: (0) 5 and "differential pair output signal".clm.
- ✓ L12: (0) 10 and "differential pair output signal".clm.

Failed

Saved

- ✓ S1: (1399) upconverter

DBs: US-PGPUB; USPAT; EPO Plurals
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10 and "differential pair output signal".clm.

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File View Edit Tools Window Help

Drafts
 BRS:
 BRS:
 BRS:
 Pending
 Active
 L1: (2108) upconverter
 L2: (900) L1 and mixer
 L3: (1) 2 and ("DC" near5 "power control")
 L4: (1) 2 and (("DC" or "direct current") near8 (power adj control\$4))
 L5: (86) 2 and (("DC" or "direct current") near8 control\$4)
 L6: (0) 5 and "differential pair output signal"
 L7: (6) 5 and ("differential pair" near8 signal)
 Failed
 Saved
 S1: (1399) upconverter
 S2: (617) S1 and mixer

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 DBs US-PGPUB; USPAT; EPO Plurals
 Default operator: OR Highlight all hit terms initially

5 and ("differential pair" near8 signal)

BRS form IS&R form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current	Ret	Inventor
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030199259 A1	20031023	ERR	A Tunable Upconverter Mixer With Image Rejection	455/141	455/130		Macedo, Jose A. et al.
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6959178 B2	20051025	11	Tunable upconverter mixer with image rejection	455/313	455/114.3; 455/118;		Macedo, Jose A. et al.
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6879816 B2	20050412	85	Integrated switchless programmable attenuator and	455/232.1	257/E27.0 46;		Bult; Klaas et al.
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6711396 B1	20040323	25	Method to reduce local oscillator or carrier leakage in	455/317	455/114.2; 455/296;		Bergsma; Adrian J. et al.
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6489816 B1	20021203	10	Frequency converter with direct current suppression	327/113			Ma; Ssu-Pin
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6377315 B1	20020423	99	System and method for providing a low power receiv	348/726	257/E27.0 46;		Carr; Frank et al.

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File View Edit Tools Window Help

Drafts
 BRS:
 BRS:
 BRS:
 Pending
 Active
 L1: (560) 455/127.1
 L2: (27) 1 and upconverter
 L3: (0) 2 and "gilbert mixer"
 L4: (0) 3 and (DC near5 "power control")
 L5: (0) 2 and ("differential pair" near8 signal)
 Failed
 Saved
 S1: (1399) upconverter
 S2: (617) S1 and mixer
 S3: (524) S2 and amplifier
 S4: (1) S3 and (DC near5 "power control")
 S5: (85) S3 and inductor
 S6: (53) S5 and resistor
 S7: (9) S6 and MESFET
 S8: (4) S7 and diode
 S9: (3) S8 and parallel
 S10: (15) S3 and (RF and MESFET)
 S11: (9) S10 and (resistor and inductor)
 S12: (8) S11 and transistor

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 DBs: US-PGPUB; USPAT: Plurals
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2 and ("differential pair" near8 signal)
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